

FIG. 1

100

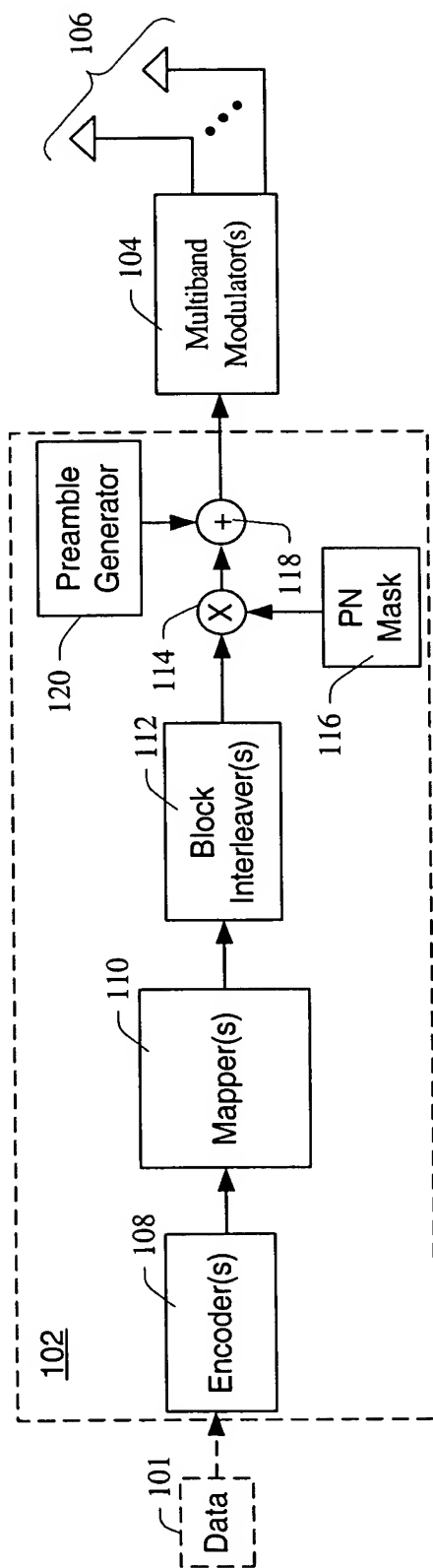


FIG. 5

500

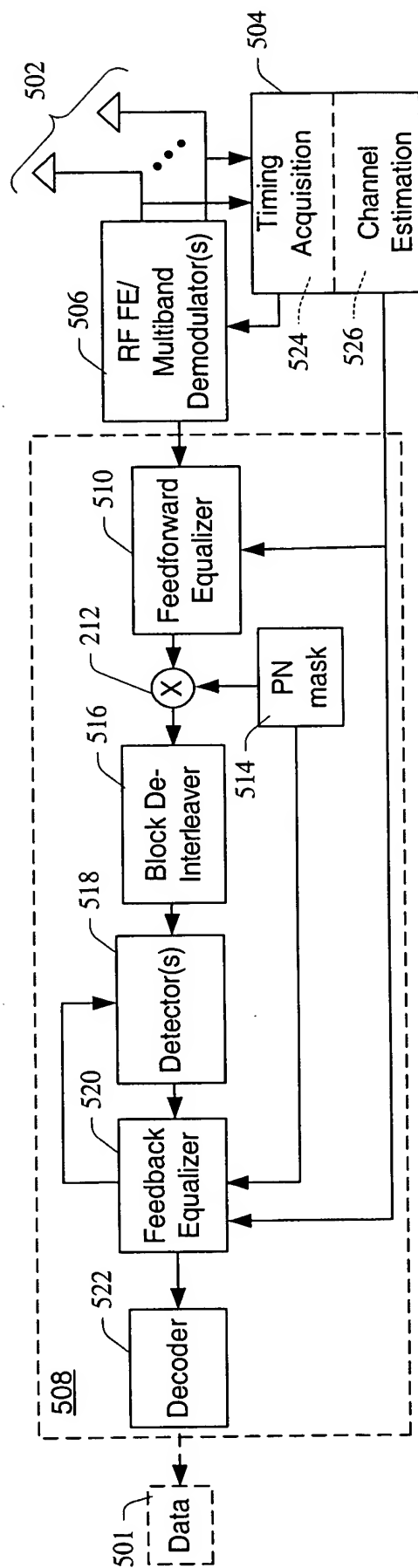
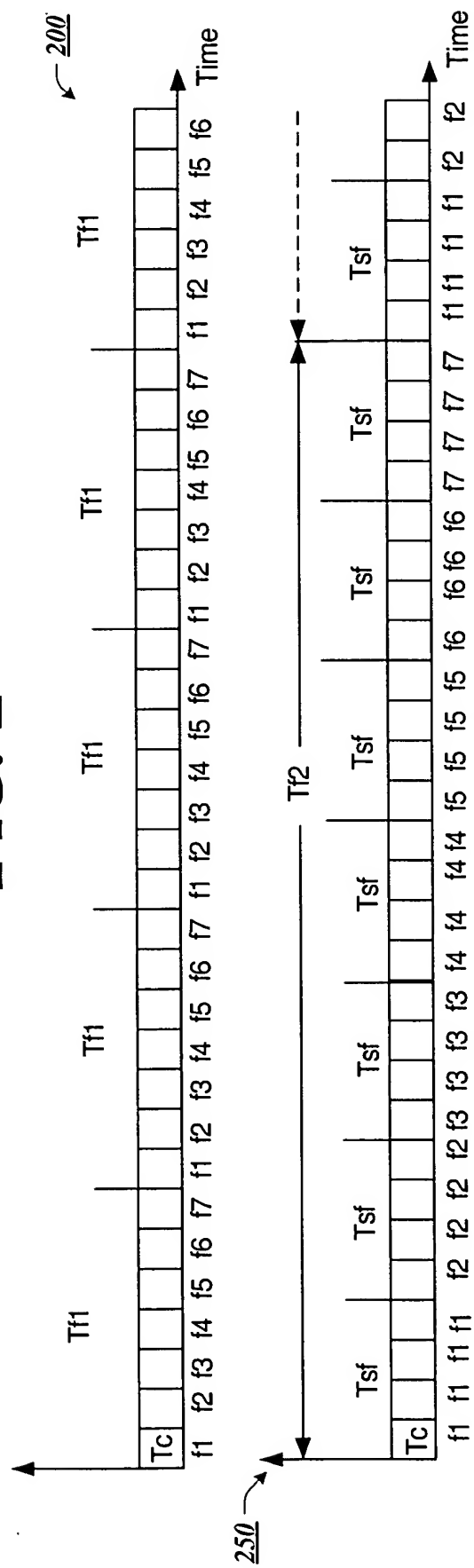


FIG. 2



Tc = chip time (e.g., ~ 3 nsec (~700 MHz BW))

$T_{f1} = T\text{-F code frame time for length 1 extension (e.g., } \sim 21 \text{ nsec)}$

Tf2 = T-F code frame time for length 4 extension (e.g., ~ 84 nsec)

Tsf = sub-frame time (e.g., ~ 12 nsec (~700 MHz BW))

FIG. 3

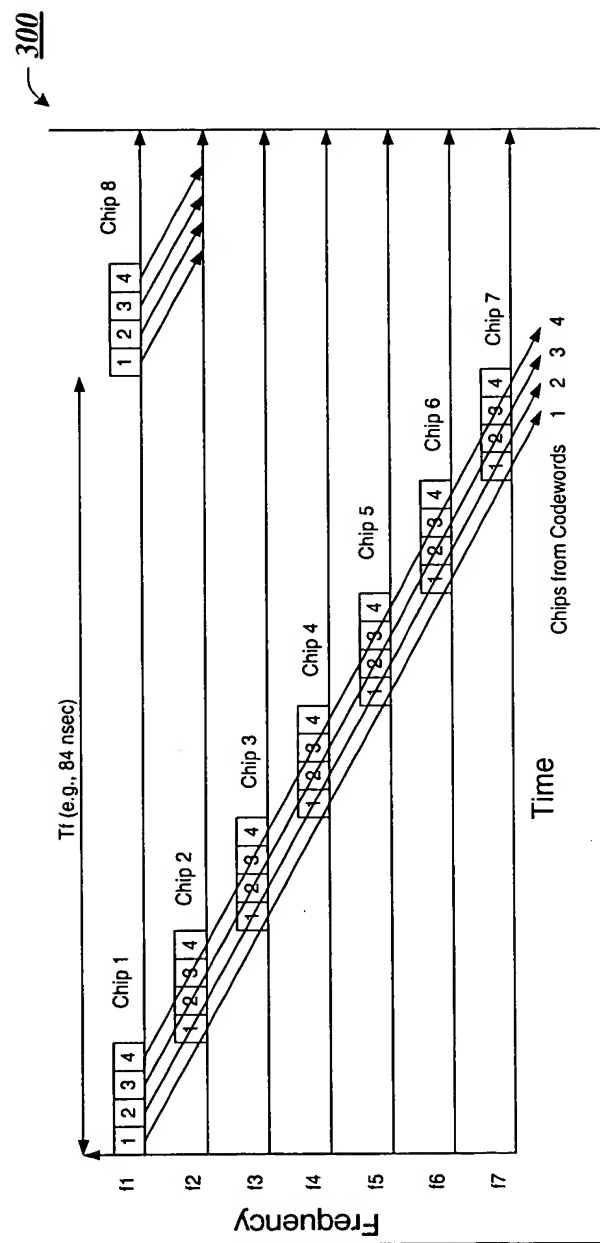


FIG. 4

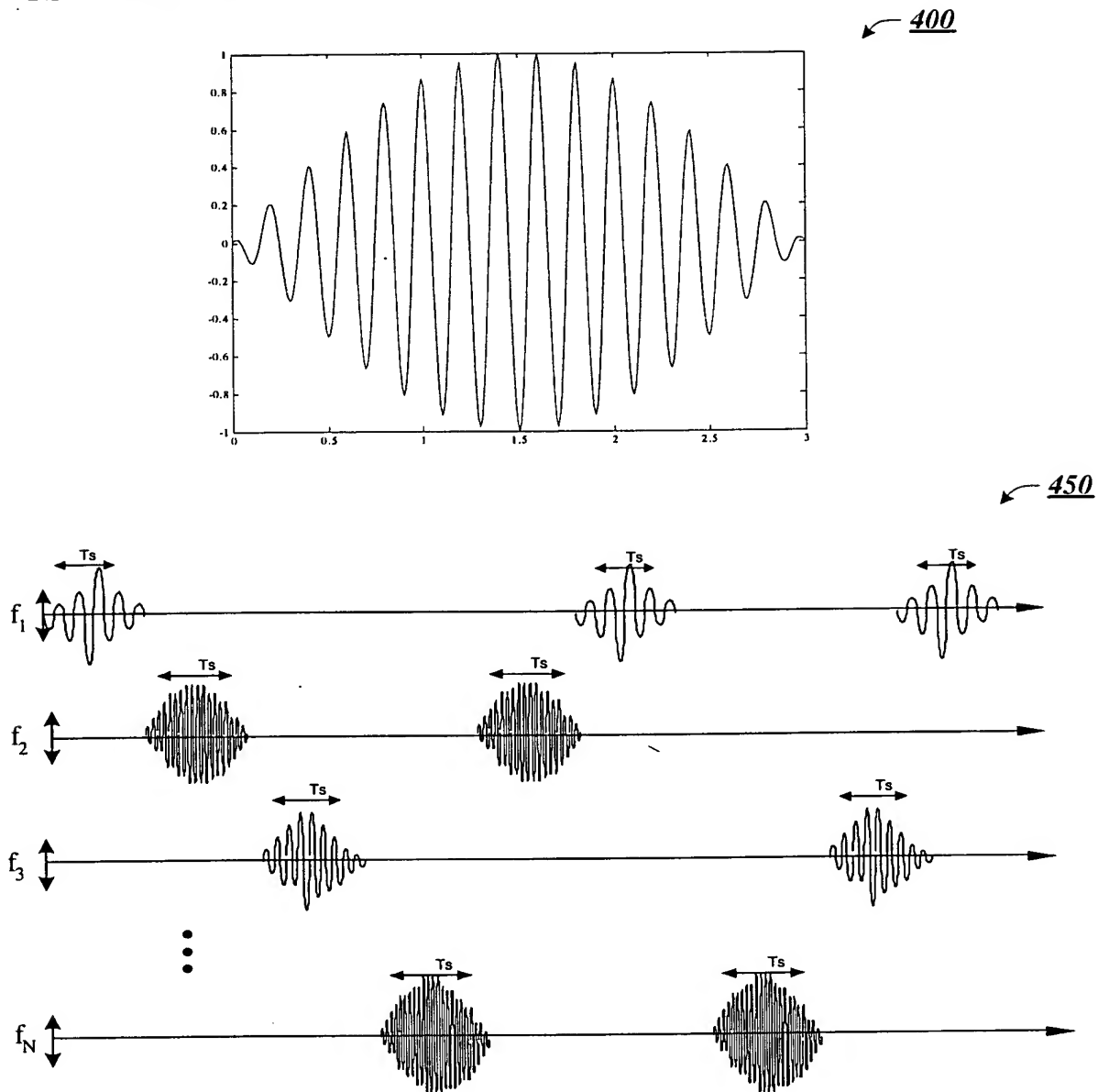


FIG. 13

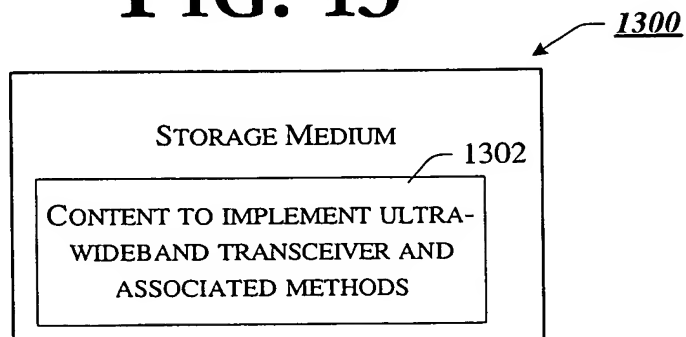


FIG. 6

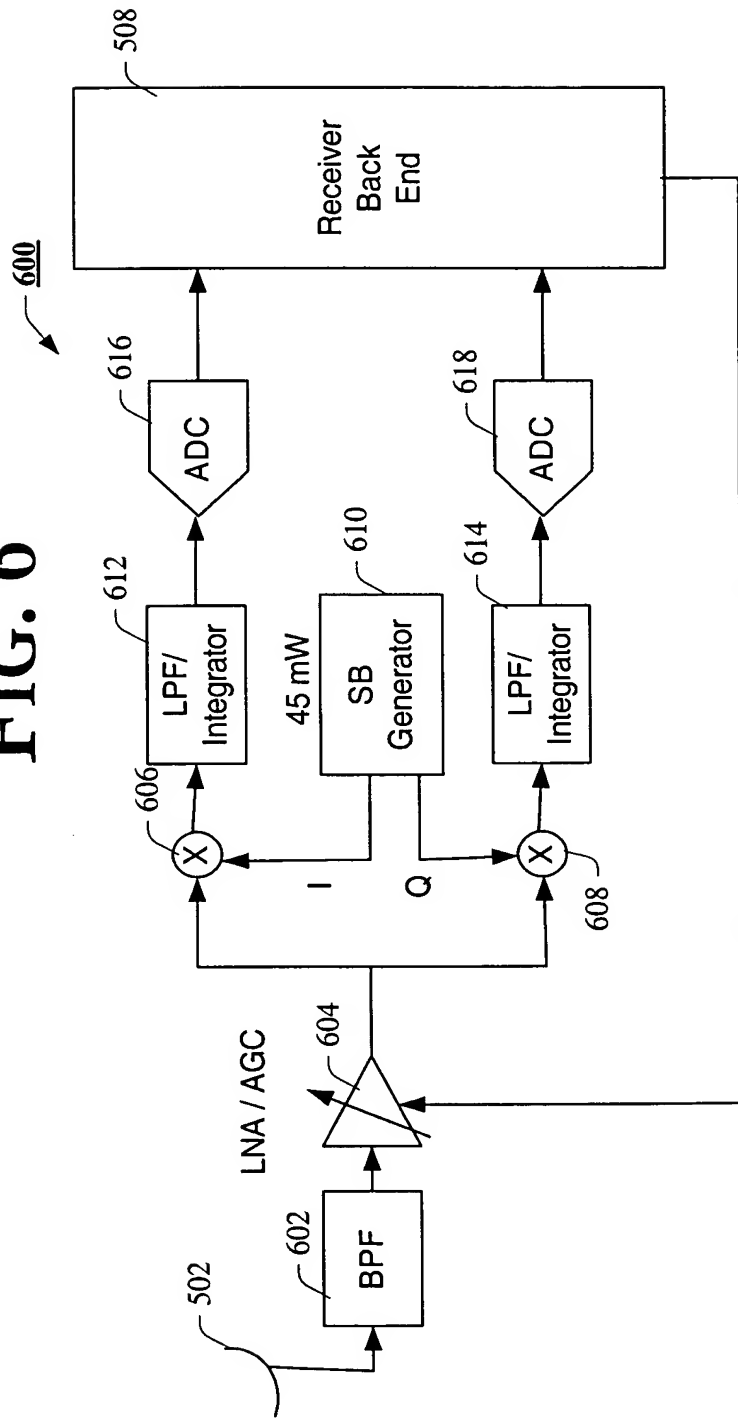


FIG 7

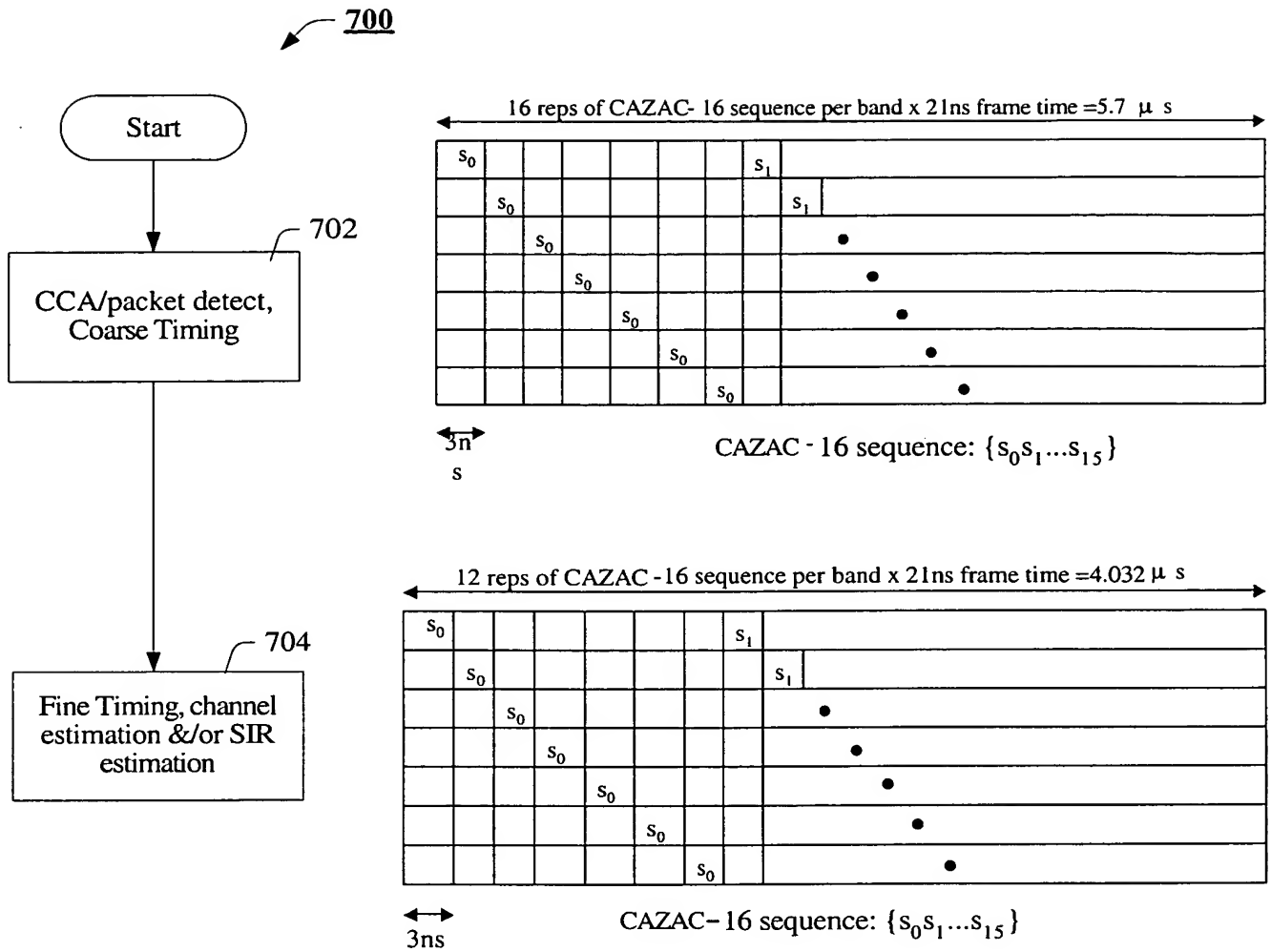


FIG. 10

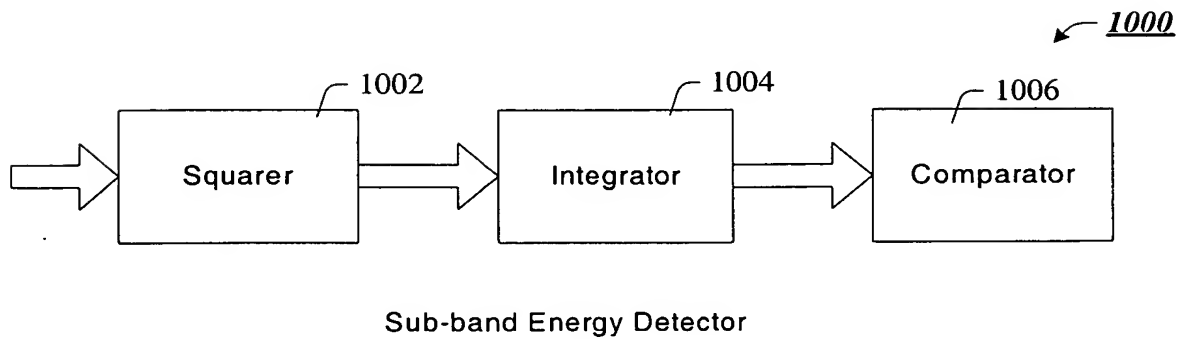


FIG. 8

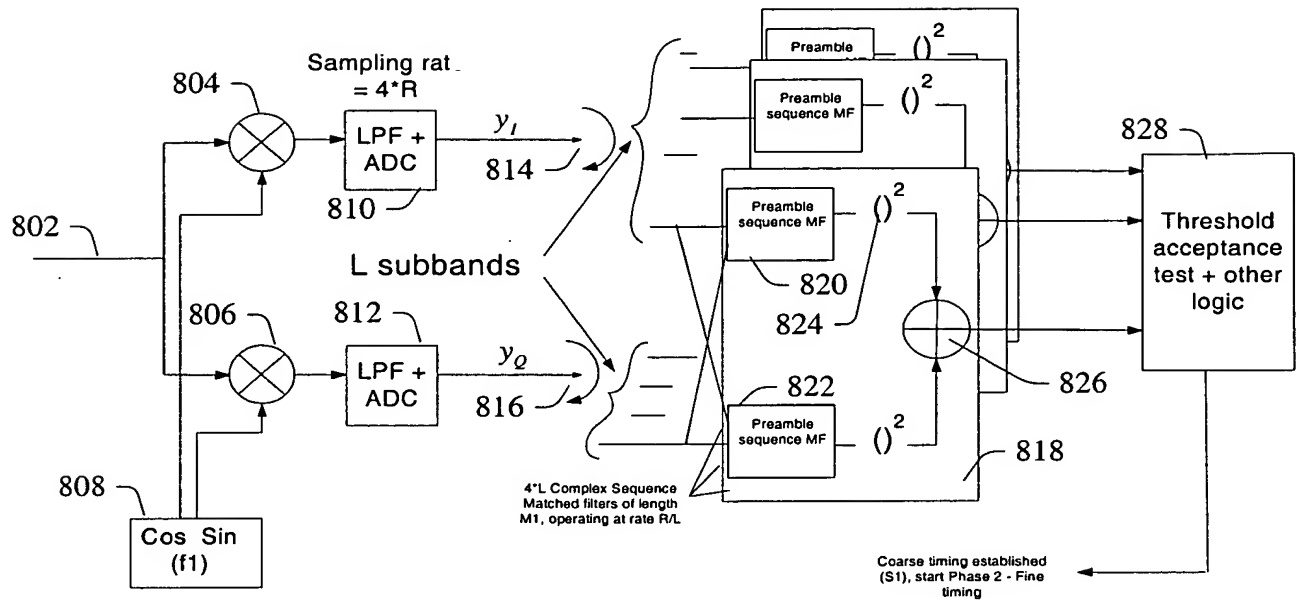


FIG. 9

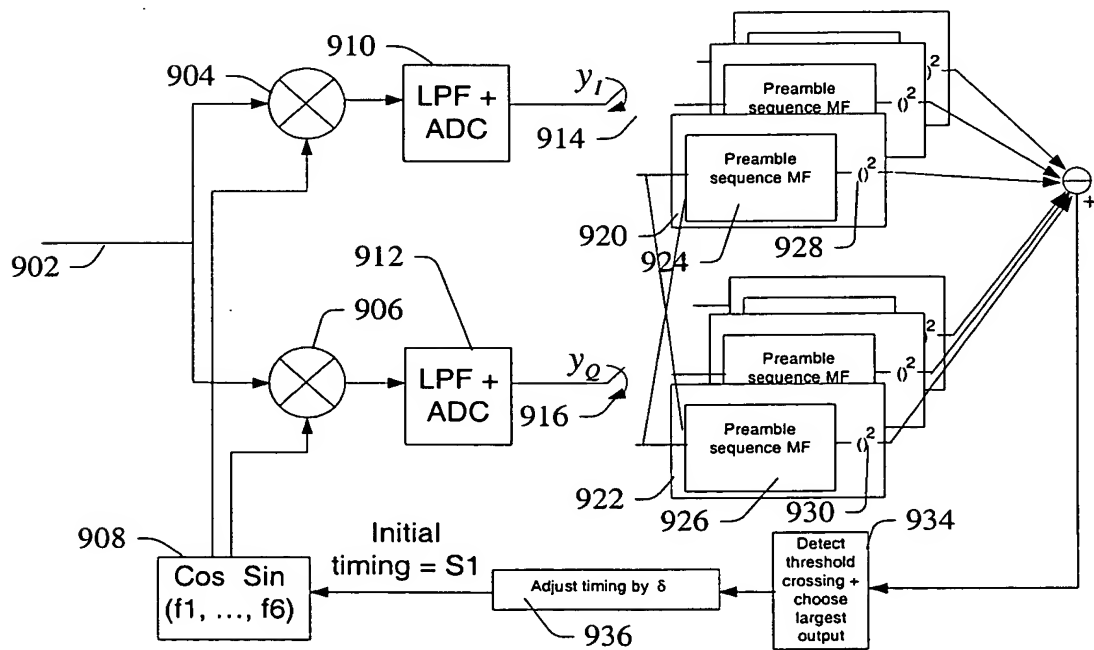


FIG. 11

1100

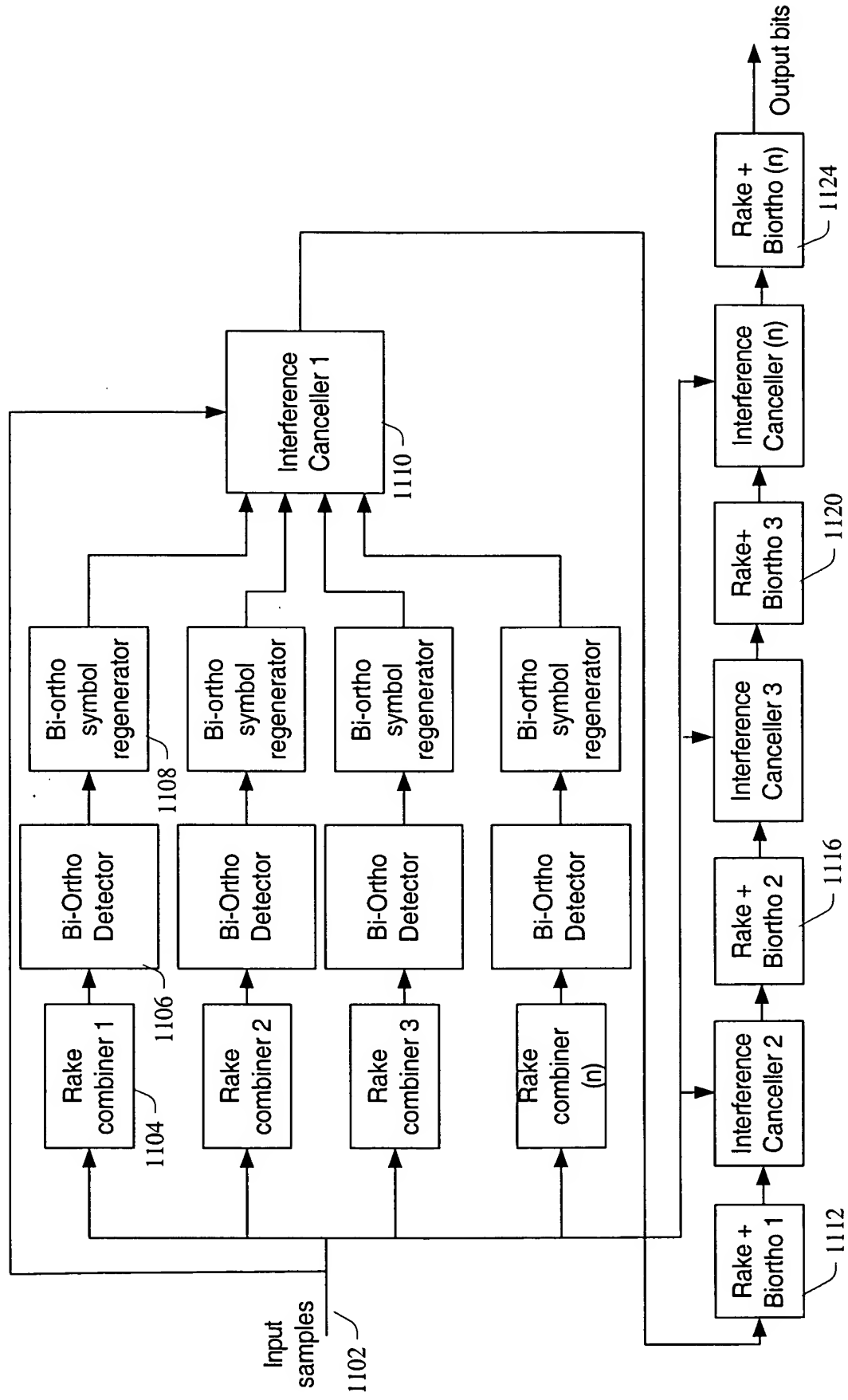


FIG. 12

